

2025-2026 Siemens EDA University Package 지원내역

주요 사용하는 EDA Tool

IC Nanometer Design Bnd SW	Design Verification Bnd SW
Advanced Node Option Op SW	Automotive-grade ATPG Op SW
Artist Link Ap SW	Catapult AI NN Op SW
Calibre ADP Op SW	Catapult-Ultra Ap SW
Calibre DESIGNrev Ap SW	FormalPro Enterprise LEC Ap SW
Calibre Interactive Ap SW	HDL Designer Ap SW
Calibre nmDRC Ap SW	Legacy Questa VIP Library Ap SW
Calibre nmDRC-H Op SW	Leonardo Insight Op SW
Calibre nmLVS Ap SW	Oasys-RTL Designer Ap SW
Calibre nmLVS-H Op SW	Oasys-RTL DFT AddOn SW
Calibre Pattern Match Op SW	Oasys-RTL Floorplanning AddOn SW
Calibre PERC Ap SW	Oasys-RTL Low Power AddOn SW
Calibre RVE/QDB-H Ap SW	Precision RTL Plus Ap SW
Calibre xACT 3D Ap SW	Precision RTL Synthesis Ap SW
Calibre xRC and xACT Ap SW	Questa CDC Ap SW
Calibre YieldAnalyzer Bnd SW	Questa CDC-FX Op SW
Calibre YieldEnhancer Bnd SW	Questa Formal AutoCheck Ap SW
xCalibrate Ap SW	Questa Formal CoverCheck Op SW
S-Edit Analysis Ap SW	Questa Formal PropCheck Op SW
Tanner Design Ap SW	Questa Formal X-Check Op SW
Tanner L-Edit IC EE Ap SW	Questa Lint
Tanner L-Edit MEMS Plus EE Ap SW	Questa Power Aware Simulation Op SW
Tanner L-Edit Photonics Ap SW	Questa Prime Ap SW
Tanner T-Spice AMS Simulator EE Bnd SW	Questa RDC Op SW
Eldo RF Op SW	Questa Secure Check
Nitro-SoC P&R Ap SW	Questa SLEC
Oasys-RTL Designer Ap SW	ReqTracer Ap SW
Oasys-RTL DFT AddOn SW	Spectrum Lev 3 ASIC-VHDL Ap SW
Oasys-RTL Floorplanning AddOn SW	Spectrum Lev 3 ASIC-VLOG Ap SW
Oasys-RTL Low Power AddOn SW	Tessent BoundaryScan-TS AC Op SW
Questa ADMS Core MixedHDL Bnd SW	Tessent BS TS Ap SW
Questa ADMS RF Op SW	Tessent CellModelGen Plus
	Tessent DefectSim
Xpedition HyperLynx Bnd SW	Tessent Diagnosis Ap SW
HyperLynx DRC 301 AP SW	Tessent IJTAG Ap SW
HyperLynx Fast 3D Solver Ap SW	Tessent LogicBIST-NS Ap SW
HyperLynx FW Solver HPC Ap SW	Tessent MemoryBIST-TS Ap SW
HyperLynx SI PI Bnd SW	Tessent MemoryBIST-TS field prog Op SW
Xpedition AMS 200 Ap SW	Tessent MemoryBIST-TS repair Op SW
Xpedition Designer 201 Ap SW	Tessent MemoryBIST-TS/NS Auto Op SW
Xpedition FPGA IO Optimizer Op SW	Tessent MissionMode Ap SW
Xpedition Layout 301 Stn	Tessent ScanPro Ap SW
Xpedition Layout Viewer Ap SW	Tessent TestKompress Ap SW
Xpedition Library Manager Ap SW	Vista Architect Stn SW
Xpedition Package Designer Ap SW (3DIC)	Visualizer Debug Environment Ap
Xpedition Substrate Integrator Ap SW (3DIC)	XLIB Creator Ap SW
	Xpedition AMS System Ap SW
	Xpedition Designer 51 Ap SW
Aprisa P&R Platform Bnd SW	Analog Mixd Sgnl Adv Bnd SW
Aprisa P&R Ap SW	EZwave AMS Waveform Processor Ap SW
Aprisa P&R Hier Op SW	AFS License Ap SW
Aprisa FinFET Op SW	Symphony Kernel Op SW